

Modern C++ in Computational Science





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- By developing scientific applications in "commodity" languages like C++, we get:
 - Compiler development and optimizations from industry
 - Community knowledge and teaching materials
 - The ability to hire "commodity" developers
- Critically, though, we lose much of this if we do not keep our codebases up to date with modern C++ patterns, features, and idioms.



DISCLAIMER

There are a ton of things happening on the C++ committee that both directly and indirectly benefit computational science and HPC. (There are a ton of things happening in general—our 2018-10 mailing was larger than the entire works of Shakespeare.) Many of the "big ticket" items have *enormous* implications for HPC, but we will not be talking about them here. Many other small things other people are doing on the committee also have a big impact on HPC, and we don't want to diminish their contributions, but it's our talk and we're going to talk about the stuff we worked on []





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 - Concepts



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 - Contracts



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- For C++ 20, we got:
 - Concepts
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 - Coroutines
 - Modules
- We did not get (but coming in C++23):
 - Networking
 - Reflection
 - Executors

std::mdspan

```
P0009
http://wg21.link/P0009r9
```

```
template <typename T, int I, int J, int K>
void three_loop_gemm(
    std::mdspan<T, I, K> a, std::mdspan<T, K, J> b, std::mdspan<T, I, J> result
)
{
    assert(a.extent(1) == b.extent(0));
    assert(a.extent(0) == result.extent(0));
    assert(b.extent(1) == result.extent(1));
    for(int i = 0; i < a.extent(0); ++i) {
        for(int j = 0; j < b.extent(1); ++j) {
            for(int k = 0; k < a.extent(1); ++k) {
                result(i, j) += a(i, k) * b(k, j);
            }
        }
    }
}</pre>
```



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        for(int j = 0; j < b.extent(1); ++j) {
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                result(i, j) += a(i, k) * b(k, j);
            }
        }
    }
}</pre>
```

std::mdspan<T, I, K> is a multidimensional view with extents I and K (both of which can be runtime-sized, using std::dynamic_extent)



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            for(int k = 0; k < a.extent(1); ++k) {
                result(i, j) += a(i, k) * b(k, j);
            }
        }
    }
}</pre>
```

These assertions can be evaluated at compile time if the extents I, J, and K are static sizes



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   assert(b.extent(1) == result.extent(1));
   for(int i = 0; i < a.extent(0); ++i) {
      for(int j = 0; j < b.extent(1); ++j) {
        for(int k = 0; k < a.extent(1); ++k) {
          result(i, j) += a(i, k) * b(k, j);
      }
   }
}</pre>
```

Indexing uses the call operator for now.

(Work is in progress to also use the subscript operator [], see https://wg21.link/p1161r2)



ISO-C++ Proposal

P0009

http://wg21.link/P0009r9

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void three_loop_gemm(
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```



 mdspan is just an alias for basic_mdspan (just like string is an alias for basic_string):

```
template<class T, ptrdiff_t... Extents>
  using mdspan = basic_mdspan<T, extents<Extents...>>;
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• The full form is much more flexible and customizable:



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ElementType is the element data type



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The full form is much more flexible and customizable:

Extents is an instance of a template std::extents<...> that contains the shape information.



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The full form is much more flexible and customizable:

LayoutPolicy is a customization point that lets you control how multi-indices are translated into memory offsets.



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```

The full form is much more flexible and customizable:

AccessorPolicy is a customization point that lets you control how memory offsets are translated into values, references, and pointers.



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```
template<class T, ptrdiff_t... Extents>
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The full form is much more flexible and customizable:





• The proposal provides three layout policies:



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 - layout_left (FORTRAN ordering)

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 - layout_right (C ordering)



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 - various forms of symmetric layouts

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 - tiled layouts
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 - sparse layouts



- The proposal provides three layout policies:
 - layout_left (FORTRAN ordering)
 - layout_right (C ordering)
 - layout_stride (non-contiguous memory)
- The customization point is flexible enough to support things like
 - tiled layouts
 - various forms of symmetric layouts
 - sparse layouts
 - compressed layouts (with the help of an AccessorPolicy)





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 - The reference type to be returned by basic_mdspan::operator()



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 - The pointer type through which access occurs



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 - A function for converting a pointer and an offset into a reference

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 - Expose non-aliasing semantics (i.e., like restrict in C)
 - Access remote memory
 - Access data stored in a compressed format of some sort
 - Access data atomically (using P0019, atomic_ref!)



std::atomic_ref

Atomic Operations on Non-Atomic Memory



ISO-C++ PROPOSAL

P0019 http://wg21.link/P0019

```
std::vector<double> my_data;
/* ... */
// Before:
atomic_fetch_add(&my_data[i], 5.0);
// After:
auto a = atomic_ref{my_data[i]};
a += 5.0;
```

Atomic Operations on Non-Atomic Memory



```
template <class T>
void my_function(std::vector<T>& my_data, T value) {
   /* ... */
   // Before:
   ????????
   // After:
   auto a = atomic_ref{my_data[i]};
   a += value;
}
```



Executors



ISO-C++ Proposal

P0443:

https://wg21.link/p0443r10

(and many more...)



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https://wg21.link/p0443r10

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• Coming in C++23



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ISO-C++ Proposal P0443: https://wg21.link/p0443r10 (and many more...)

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 - (Why should you care about something coming that far away?)
- One of the most ambitious generic programming exercises ISO-C++ has ever undertaken



P0443:
https://wg21.link/p0443r10

(and many more...)

- Coming in C++23
 - (Why should you care about something coming that far away?)
- One of the most ambitious generic programming exercises ISO-C++ has ever undertaken
- Provides a generic abstraction for the execution model in the presence of a restricted programming model

```
template <class DataContainer>
DataContainer my_algorithm(DataContainer& data) {
   apply_transformation(data);
   DataContainer result = allocate_result_container_for(data);
   apply_reduction(data, result);
   return result;
}
```

```
apply_transformation(data);
```

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```
(1)
```

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```
template <class Executor, class DataContainer>
DataContainer my_algorithm(Executor ex, DataContainer& data) {
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   return result;
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```

```
template <class Executor>
DataContainer my_outer_loop(Executor network_executor) {
 auto data = get_neighbor_info(network_executor);
 // if it's large enough, put it on the GPU
 if(data.size() > THRESHOLD) {
   auto gpu_executor = get_nearest_gpu_executor(network_executor);
   auto gpu_data = migrate_data(std::move(data), network_executor, gpu_executor);
   auto gpu result = my algorithm(gpu executor, gpu data);
   auto result = migrate_data(std::move(gpu_result), gpu_executor, network_executor);
   return result;
 else {
   // Otherwise, do it in place
   auto result = my_algorithm(network_executor, data);
   return result;
```

```
auto data = get_neighbor_info(network_executor);
```

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// if it's large enough, put it on the GPU
if(data.size() > THRESHOLD) {
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18



```
else {
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 auto result = my_algorithm(network_executor, data);
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```

```
if(data.size() > THRESHOLD) {
```

But this isn't very generic...

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   auto gpu_data = migrate_data(std::move(data), network_executor, gpu_executor);
   auto gpu_result = my_algorithm(gpu_executor, gpu_data);
   auto result = migrate_data(std::move(gpu_result), gpu_executor, network_executor);
   return result;
 else {
   // Otherwise, do it in place
   auto result = my_algorithm(network_executor, data);
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template <class Executor>
DataContainer my_outer_loop(Executor network_executor) {
   auto data = get_neighbor_info(network_executor);
   // if it's large enough, put it on the GPU
   auto gpu_executor = get_nearest_gpu_executor(network_executor);
   auto threshold = std::query(gpu_executor, transfer_threshold(data, network_executor));
   if(data.size() > threshold) {
     auto gpu_data = migrate_data(std::move(data), network_executor, gpu_executor);
     auto gpu_result = my_algorithm(gpu_executor, gpu_data);
     auto result = migrate_data(std::move(gpu_result), gpu_executor, network_executor);
     return result;
   }
   else {
     auto result = my_algorithm(network_executor, data);
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        auto result = migrate_data(std::move(gpu_result), gpu_executor, network_executor);
        return result;
    }
    else {
        auto result = my_algorithm(network_executor, data);
        return result;
    }
}
```

Solution: Put the customization on the executor!



```
template <class Executor>
DataContainer my_outer_loop(Executor network_executor) {
   auto data = get_neighbor_info(network_executor);
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   auto threshold = std::query(gpu_executor, transfer_threshold(data, network_executor));
   if(data.size() > threshold) {
     auto gpu_data = migrate_data(std::move(data), network_executor, gpu_executor);
     auto gpu_result = my_algorithm(gpu_executor, gpu_data);
     auto result = migrate_data(std::move(gpu_result), gpu_executor, network_executor);
     return result;
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   else {
     auto result = my_algorithm(network_executor, data);
     return result;
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}
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DataContainer my_outer_loop(Executor network_executor) {
 auto data = get_neighbor_info(network_executor);
 // if it's large enough, put it on the GPU
 auto gpu_executor = get_nearest_gpu_executor(network_executor);
 auto cost_model = std::query(my_algorithm, cost_model(network_executor, gpu_executor));
 auto should_transfer = std::query(cost_model, transfer_recommendation(data));
 if(should transfer) {
   auto gpu_data = migrate_data(std::move(data), network_executor, gpu_executor);
   auto gpu_result = my_algorithm(gpu_executor, gpu_data);
   auto result = migrate data(std::move(gpu result), gpu executor, network executor);
   return result;
 else {
   auto result = my_algorithm(network_executor, data);
   return result;
```

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// if it's large enough, put it on the GPU
auto gpu_executor = get_nearest_gpu_executor(network_executor);
auto cost_model = std::query(my_algorithm, cost_model(network_executor, gpu_executor));
auto should_transfer = std::query(cost_model, transfer_recommendation(data));
if(should transfer) {
```

Even better: ask the algorithm!

1

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(1)

Executor Example

What happens if I have a network-capable GPU direct executor?

```
void my_program() {
  auto gpu_direct_executor = /*...*/;
  // ...
  for(auto iter : my_iterations) {
      // ...
      auto result = my_outer_loop(gpu_direct_executor);
      // ...
  }
}
```

What happens if I have a network-capable GPU direct executor?

```
auto gpu_direct_executor = /*...*/;
 auto result = my_outer_loop(gpu_direct_executor);
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```
void my_program() {
  auto gpu_direct_executor = /*...*/;
  // ...
  for(auto iter : my_iterations) {
      // ...
      auto result = my_outer_loop(gpu_direct_executor);
      // ...
  }
}
```

How does this change how you should write code?



How does this change how you should write code?



• Use algorithms, not loops

How does this change how you should write code?

- Use algorithms, not loops
- Write to the most restricted programming model you can

How does this change how you should write code?

- Use algorithms, not loops
- Write to the most restricted programming model you can
- Use Kokkos (or something similar that is tracking standards for you)



Questions?