

PAPI

PAPI (Performance Application Programming Interface) provides the tool designer and application engineer with a consistent interface and methodology for use of the performance counter hardware found in most major microprocessors. In addition, it provides access to a collection of components that expose performance measurement opportunities across the hardware and software stack.

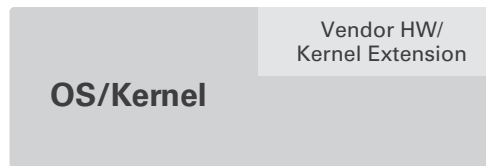
PERFORMANCE ANALYSIS TOOLS

← Vampir
TAU →
← HPCView

- MORE**
- HPCToolkit
 - Open|SpeedShop
 - PaRSEC
 - PerfSuite
 - Scalasca
 - SCORE-P
 - MuMMI

STANDARD FEATURES

- ▶ Standardized Performance Metrics
- ▶ Easy Access to Platform-Specific Metrics
- ▶ Multiplexed Event Measurement
- ▶ Dispatch on Overflow
- ▶ Overflow & Profiling on Multiple Simultaneous Events
- ▶ Bindings for C, Fortran and Matlab
- ▶ User Definable Metrics derived from Platform-Specific Metrics
- ▶ Support for Virtual Computing Environments
- ▶ Performance counter monitoring at task granularity for dataflow runtime PaRSEC



SUPPORTED ARCHITECTURES

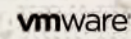
- AMD**
- ARM** Cortex A8, A9, A15, X-Gen (ARM64)
- CRAY**
- IBM** Blue Gene Series, EMON power on BG/Q, Power Series
- Infiniband**
- Intel** Nehalem, Westmere, Sandy Bridge, Ivy Bridge, Haswell, Knights Corner
Coming Soon: Broadwell
RAPL, Power on Xeon Phi
- NVidia** Tesla, Kepler, NVML



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